

Please amend claims 11, 17, 21, 23, 26-28, 30, 32, 33 and 34 as follows:

11. (Amended) A method for generating a switching frequency in a power conversion system, comprising:

generating a primary current;

a<sup>1</sup> cycling one or more secondary current sources to generate a secondary current which varies over time; and

[supplying the primary and secondary currents]combining the secondary current with the primary current to be received at a control input of an oscillator for generating a switching frequency which is varied over time.

17. (Amended) A method for generating a switching frequency in a power conversion system, comprising:

a<sup>2</sup> generating a primary voltage;

cycling one or more secondary voltage sources to generate a secondary voltage which varies over time; and

[supplying the primary and secondary voltages]combining the secondary voltage with the primary voltage to be received at a control input of a voltage-controlled oscillator for generating a switching frequency which is varied over time.

21. (Amended) A frequency jittering circuit for varying a power supply switching frequency, comprising:

a<sup>3</sup> an oscillator for generating a signal having a switching frequency, the oscillator having a control input for varying the switching frequency; and

means coupled to the control input for varying the switching frequency, including:

one or more current sources coupled to the control input; and

a counter coupled to the output of the oscillator and to the one or more current sources.

<sup>22</sup>23. (Amended) The circuit of claim [22]21 wherein the oscillator further comprises:

a primary current source coupled to the control input; and  
a differential switch coupled to the primary current source.

<sup>27</sup>26. (Amended) The circuit of claim [22]21 further comprising a transistor coupled to each current source and to the counter.

<sup>25</sup>27. (Amended) The circuit of claim [22]<sup>22</sup>23 wherein the primary current source generates a current I and each of [the]said one or more current sources generates a current lower than I.

<sup>26</sup>28. (Amended) The circuit of claim [22]<sup>22</sup>23 wherein the primary current source generates a current I and each of [the]said one or more current sources generates a second current lower than the current I, further comprising a transistor coupled to each current source connected to the counter.

<sup>28</sup>30. (Amended) The circuit of claim [22]21 wherein the oscillator further comprises:  
a primary voltage source coupled to the control input; and a differential switch coupled to the primary voltage source.

<sup>30</sup>~~32~~ (Amended) The circuit of claim <sup>29</sup>~~31~~ further comprising:

one or more comparators coupled to the capacitor; and

[the] means coupled to the capacitor for alternately charging and discharging the capacitor.

<sup>31</sup>~~33~~ (Amended) A power supply having a transformer coupled to an input voltage, the transformer having a primary winding, the power supply comprising:

an oscillator for generating a signal having a frequency, the oscillator having a control input for varying the frequency of the signal, the oscillator including:

a primary current source coupled to the control input;

a differential switch coupled to the primary current source;

a capacitor coupled to the differential switch; and

a comparator coupled to the differential switch;

a digital to analog converter coupled to the control input, the [analog to] digital to analog converter having one or more current sources, wherein the primary current source generates a current I and each of [the]said one or more current sources generates a current lower than I;

a counter coupled to the output of the oscillator and to the current sources of the digital to analog converter; and

a power transistor coupled to the oscillator and to one terminal of the primary winding, the power transistor modulating its output in providing a regulated power supply output.

<sup>32</sup>~~34~~ (Amended) A power supply having a transformer coupled to an input voltage, the transformer having a primary winding, the power supply comprising: